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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10081963	FILING DATE 02/21/2002	CLASS 438	SUBCLASS 745	GAU 2812	EXAMINER 1765 J. Tech
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**APPLICANTS: Geist Jon;

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no	Yel
Verified and Acknowledged Examiner's initials		
TITLE: Method for TMAH etching of CMOS integrated circuits		U.S. DEPT. OF COMM./PAT & TM-PTO-438 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drwg.	Figs. Drwg.
Primary Examiner		Print Fig.	
PREPARED FOR ISSUE		Application Examiner	
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